Global clock source
VCTCXO
\( f_h \)
\( \frac{1}{N} \)
\( f_1 \)
Differential interface

Acquisition node 1
Data acquisition
\( f_{1d} \)
PLL frequency doubling
\( f_{1h} \)
Differential interface

Acquisition node m
Data acquisition
\( f_{md} \)
PLL frequency doubling
\( f_{mh} \)
Differential interface